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Jinny Nguyen
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TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: December 19, 2002

~~Mark~~ RANDOLPH

~~Serial~~ No.: 10/032,646

Group Art Unit: 2811

~~Filed~~: December 27, 2001

Examiner: Loke, Steven Ho Yin

~~For~~: PLANAR TRANSISTOR STRUCTURE USING ISOLATION IMPLANTS FOR IMPROVED VSS RESISTANCE AND FOR PROCESS SIMPLIFICATION

#3/Election
P.T.
1/10/03

Assistant Commissioner of Patents
Washington, D.C. 20231

RESPONSE

In response to the Office Action dated December 12, 2002 please amend the above-identified application in the following manner:

REMARKS

This Response is in response to the Office Action dated December 12, 2002. Claims 1-10 are pending in the present application.

In the above-mentioned Office Action, the Examiner stated that claims 1-3, and 4-10 are subject to a restriction requirement under 35 U.S.C. § 121. In particular, the Examiner stated that